

VISIBLE LIGHT PRODUCTS SPECIFICATION

HTLB2-48ABD



Drawn by	Checked by	Approved by



DATE:2008/10/17

REV:I



HUEY JANN ELECTRONICS INDUSTRY CO., LTD.

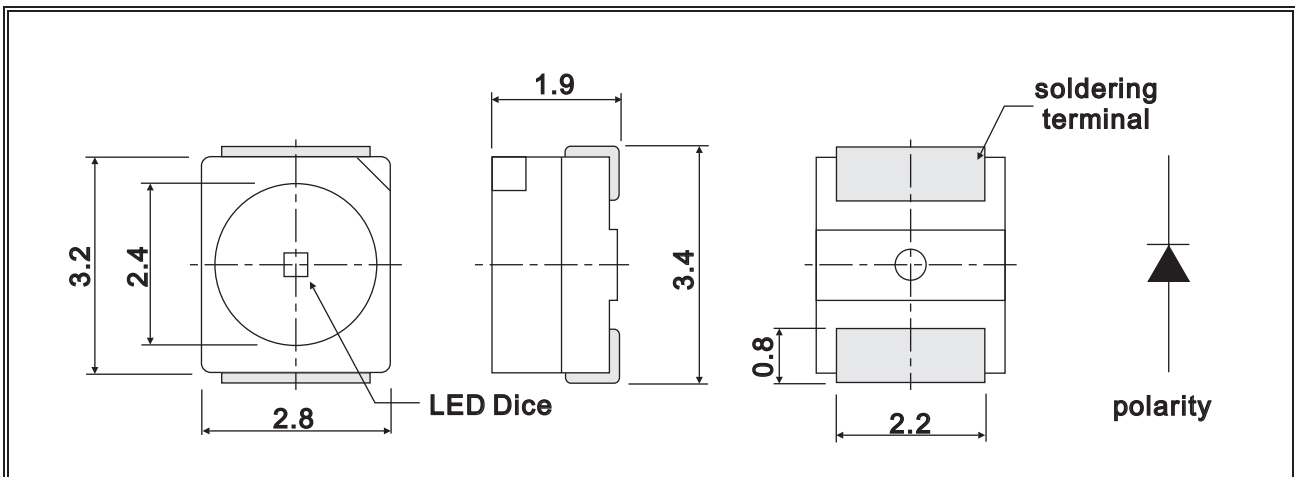
No.27 Line 466 Sec.2,Cannng-nan Rd. Wu-chi Town Taichung Shien, Taiwan, R.O.C.

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DEVICES

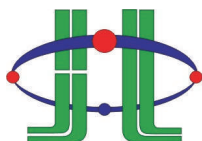
Part Number	Lens		Source	
	Color	Diffusion	Dice Source	Color
HTLB2-48ABD	Water clear	Non-Diffused	GaN/InGa	Super Blue

PACKAGE DIMENSIONS



NOTE:

- 1.All dimensions are in millimeter.
- 2.specifications are subject to change without notice.
- 3.Tolerance is $\pm 0.3\text{mm}$ unless otherwise noted.

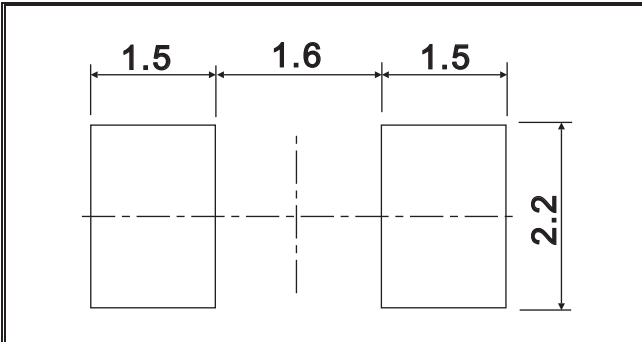


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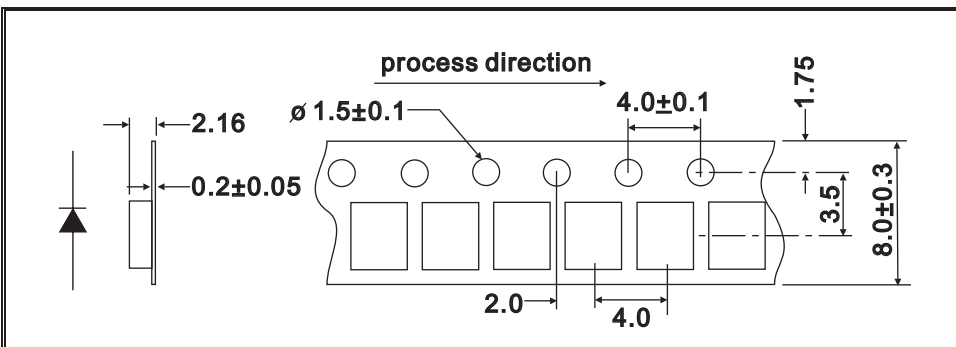
RECOMMENDED SOLDERING PATTERN



NOTE:

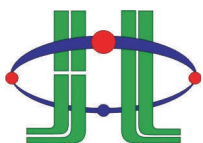
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TAPE SPECIFICATIONS



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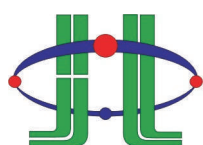
ABSOLUTE MAXIMUM RATINGS

TA=25°C

PARAMETER	SYMBOL	MAX. RATING	UNIT
Power Dissipation	Pd	120	mW
Continuous Forward Current	IF	20	mA
Peak Forward Current *1	IFM	100	mA
Reverse Voltage	VR	4	V
Electrostatic Discharge(HBM)	ESD	150	V
LED Junction Temperature	Tj	100	°C
Operating Temperature	Topr	-40 ~ +85	°C
Storage Temperature	Tstg	-40 ~ +85	°C
Reflow Soldering (preheat 150-180°C 60-120sec, soldering temp 260°C 5sec)			

*1.Duty Ratio=0.1%,Pulse Width=10us.

*2.Iron soldering in 350°C within 5 seconds will not cause damage to the dice. But be aware of the high temperature will not only make the epoxy soften but also cause the lead moving and the gold wire broken and even open. So before returning to the normal temperature PLEASE AVOID any serious pressure on the top of epoxy and lead.



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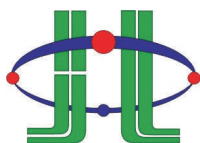
ELECTRIC-OPTICAL CHARACTERISTICS

TA=25°C

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
View Angle of Half Power	2θ1/2	IF=20mA		120		deg
Forward Voltage	VF			3.3	3.8	V
Reverse Current	IR	VR=5V			60	μA
Luminous Intensity *2	IV	IF=20mA	120	210		mcd
Peak Emission Wavelength	λp			462		nm
Dominate Wave Length *3	λd(HUE)			465		nm
Spectrum Width Of Half Valve	Δλ			35		nm

*2.Tolerance:±15% HUEY-JANN measuring equipment : EXELTRON 2001. 2.S370 made by U.D.T.

*3.The dominate wavelength , λ d, is derived from the CIE Chromaticity Diagram and represents the color of the device.



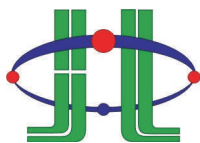
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RELIABILITY TEST

Classification	Test Item	Test Conditions	Refer to Temperature-profile
Reflow Soldering	Lead Solder	Pre-heat=120~150°C Pre-heat time=120sec Max Peak temperature=240°C Max Soldering time=10sec Max	
	Lead free Solder	Pre-heat=180~200°C Pre-heat time=120sec Max Peak temperature=260°C Max Soldering time=10sec Max	
Hand Soldering	Temperature=350°C Max Soldering time=3sec Max(one time only)		

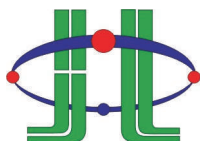
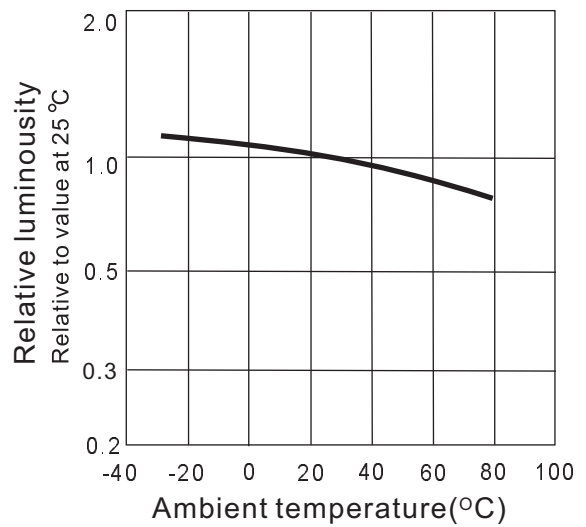
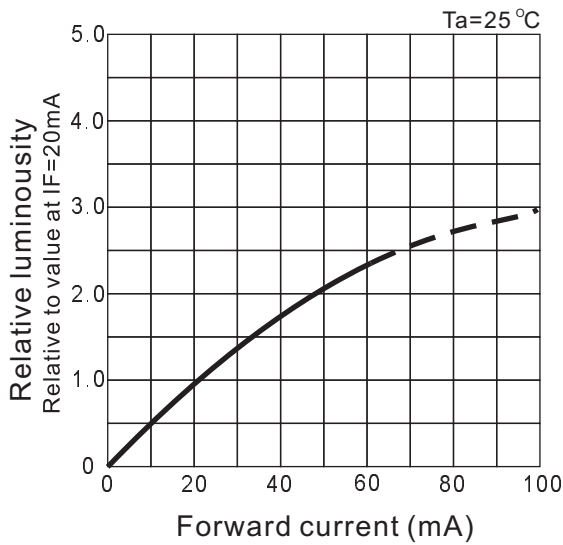
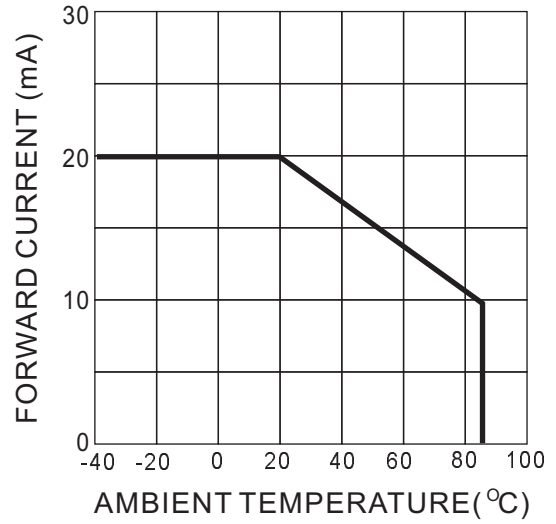
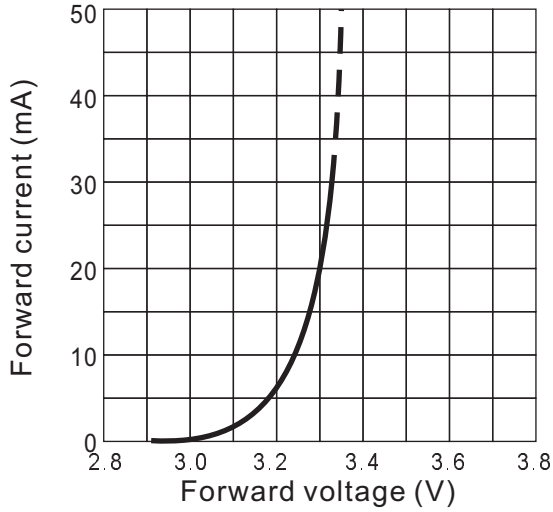


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TYPICAL ELECTRICAL OPTICAL CHARACTERISTICS CURVES

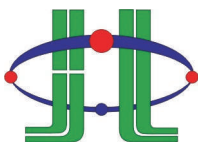
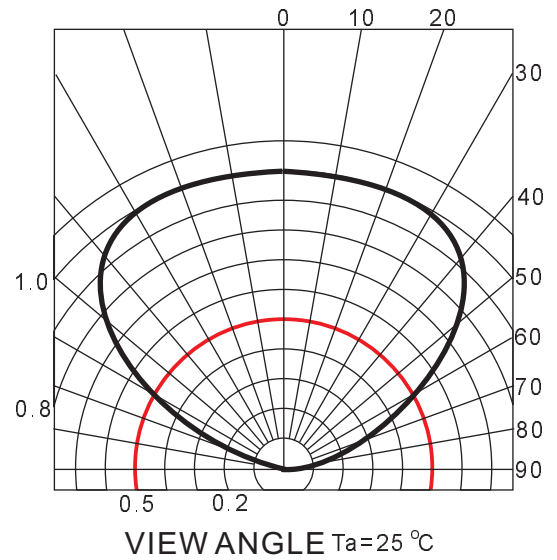
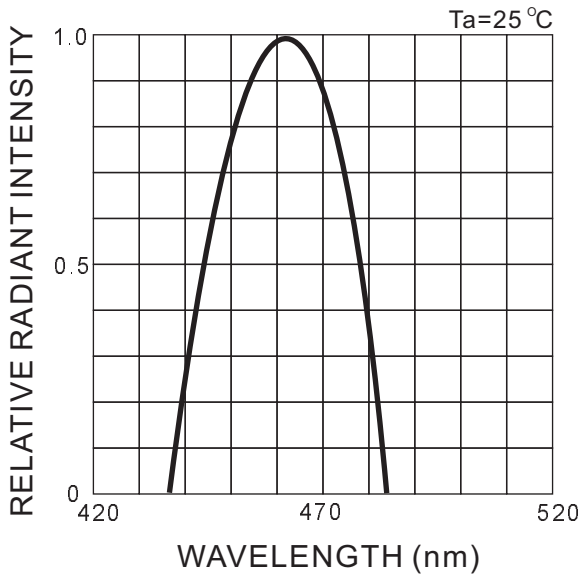


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LED VF Bin Selection

$I_F=20\text{mA}$

BIN CODE	Forward Voltage	
	Minimum	Maximum
j	3.0	3.2
k	3.2	3.4
m	3.4	3.6
n	3.6	3.8

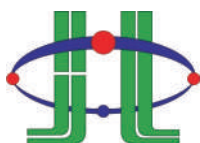
Voltage tolerance for each bin limit is $\pm 0.03\text{V}$

Brightness Bin Selection

$I_F=20\text{mA}$

BIN CODE	Brightness in mcd	
	Minimum	Maximum
F	108	140
G	140	180
H	180	240
J	240	310

Brightness tolerance for each bin limit is $\pm 15\%$



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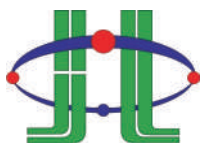
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Color Bin Selection

$I_F=20mA$

Bin	Dominate Wavelength	
	Minimum	Maximum
3	455.0	460.0
4	460.0	464.0
5	464.0	467.0
6	467.0	470.0
7	470.0	473.0

Color tolerance for each bin limit is $\pm 0.5nm$



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